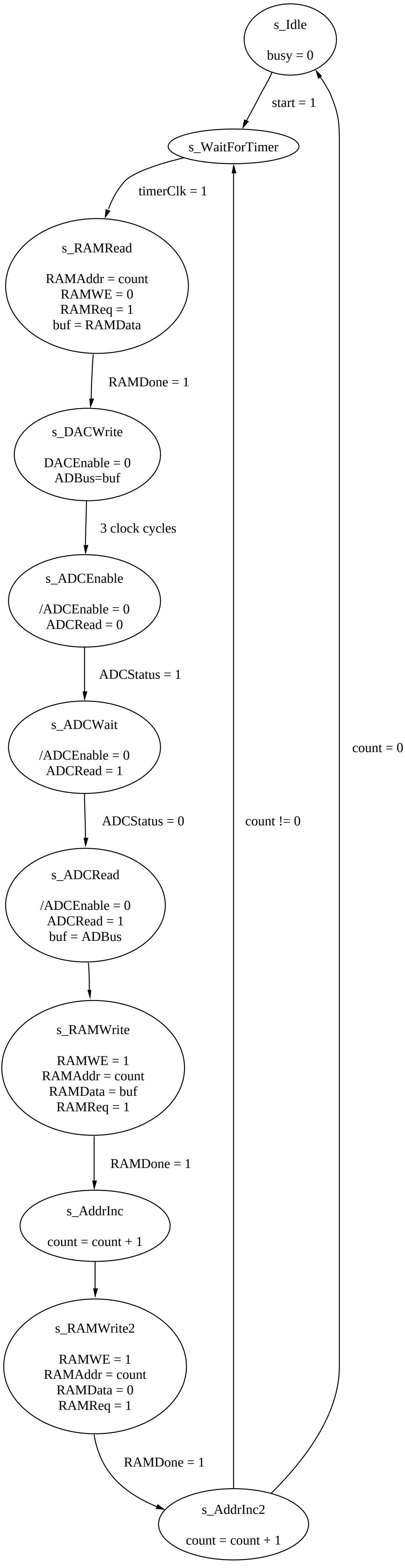
s_Idle
busy = 0
start = 1
s_WaitForTimer
timerClk = 1
s_RAMRead
RAMAddr = count
RAMWE = 0
RAMReq = 1
buf = RAMData
RAMDone = 1
s_DACWrite
DACEnable = 0
ADBus=buf
3 clock cycles
s_ADCEnable
/ADCEnable = 0
ADCRead = 0
ADCStatus = 1
s_ADCWait
/ADCEnable = 0
ADCRead = 1
ADCStatus = 0
count != 0
count = 0
s_ADCRead
/ADCEnable = 0
ADCRead = 1
buf = ADBus
s_RAMWrite
RAMWE = 1
RAMAddr = count
RAMData = buf
RAMReq = 1
RAMDone = 1
s_AddrInc
count = count + 1
s_RAMWrite2
RAMWE = 1
RAMAddr = count
RAMData = 0
RAMReq = 1
RAMDone = 1
s_AddrInc2
count = count + 1