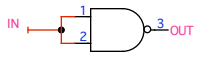
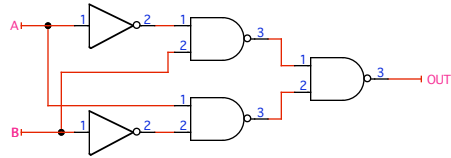


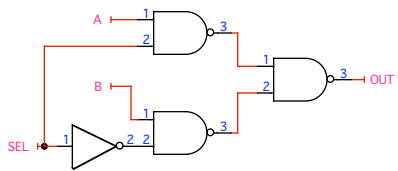
a. inverter



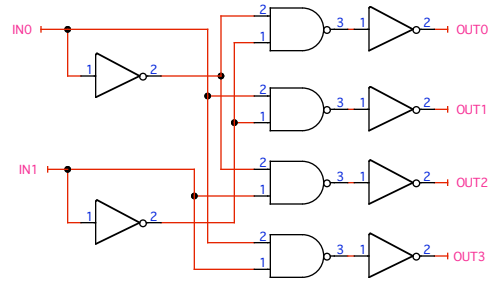
b. 2-input XOR



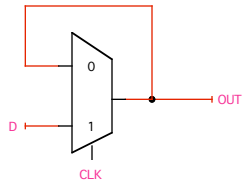
c. 2-to-1 mux



d. 2-to-4 decoder



e. transparent latch



f. flip-flop

